

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Izuo Iida
Serial No. : 09/876,554
Filed : June 7, 2001
Title : METHOD OF MANUFACTURING SEMICONDUCTOR DEVICE

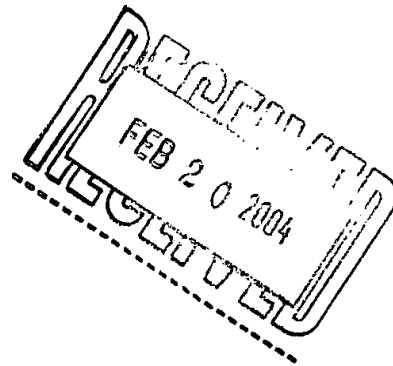
Art Unit : 2815
Examiner : N. Richards

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

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PRELIMINARY AMENDMENT

Prior to examination, please amend the application as indicated on the following pages.



CERTIFICATE OF MAILING BY FIRST CLASS MAIL

I hereby certify under 37 CFR §1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

February 3, 2004

Date of Deposit

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